

## **IN THE SPECIFICATION**

Please amend the paragraph beginning at page 16, line 25 as follows:

An activity detector is shown in Figure 17 and may be considered in four stages.

- Stage 1, Re-clocking stage, comprises a T' flip-flop. It functions as a frequency divider. The digital amplifier includes a clock modulator and clock activity is monitored by counting divided multiples of the clock. The main advantage of this approach is the output CLK1 will approximately become a 50% duty cycle waveform for a pulse train of the input at CLK\_IN ranging from 7% to 93% duty cycle.